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#4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: David G. FIGUEROA, ET AL.

Serial No.: 10/090,735

Filed: March 6, 2002

For: ARRANGEMENTS FOR MINIMIZING SIGNAL
PATH DISCONTINUITIES

*HG
Formal
Drawings
L. Fischer
4-3-03*

SUBMISSION OF FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

May 31, 2002

Sir:

Applicants submit herewith four (4) sheets of Formal Drawings consisting of Fig. 1-5 in connection with the above-identified application.

Respectfully submitted,

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FIG. 1

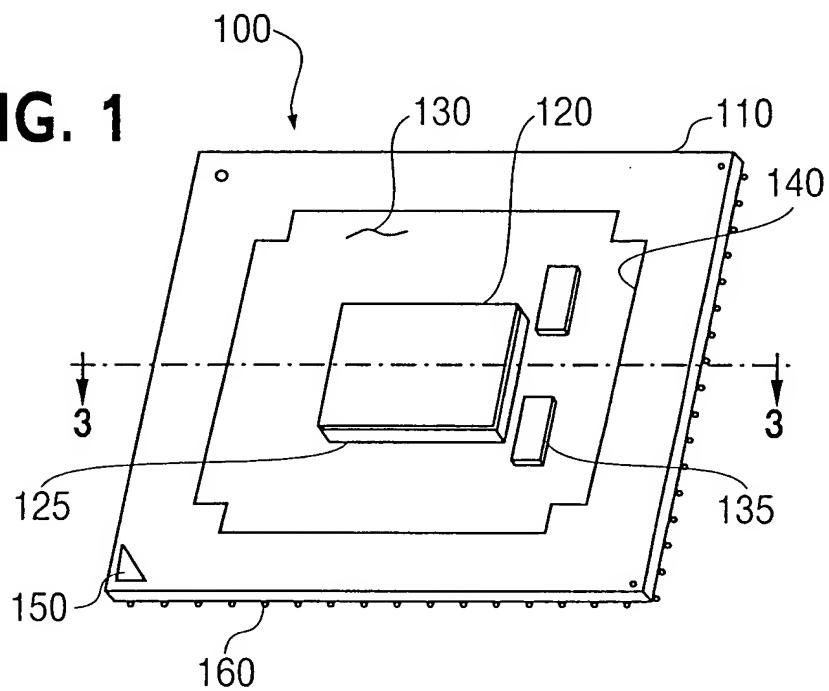
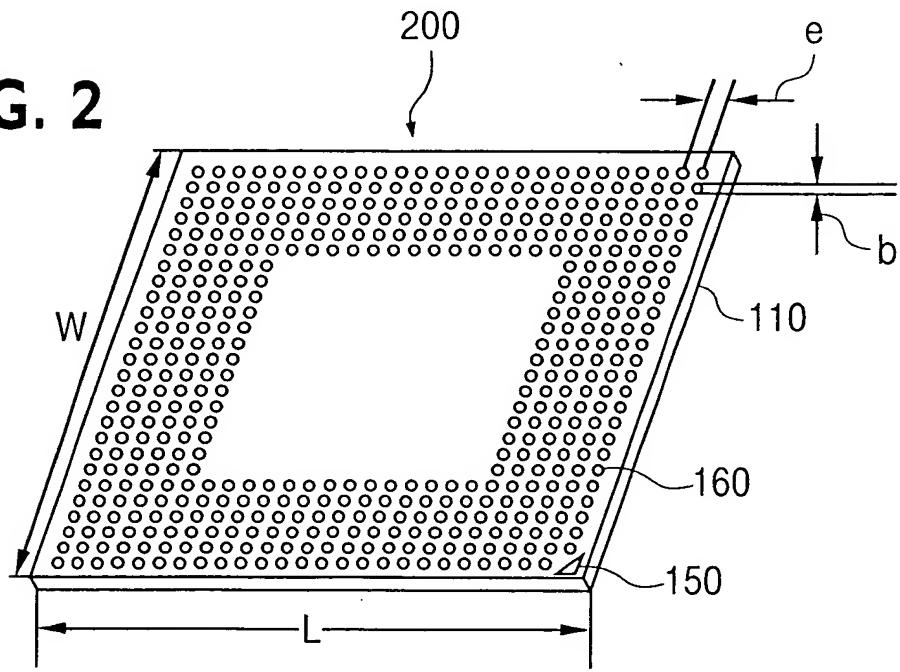
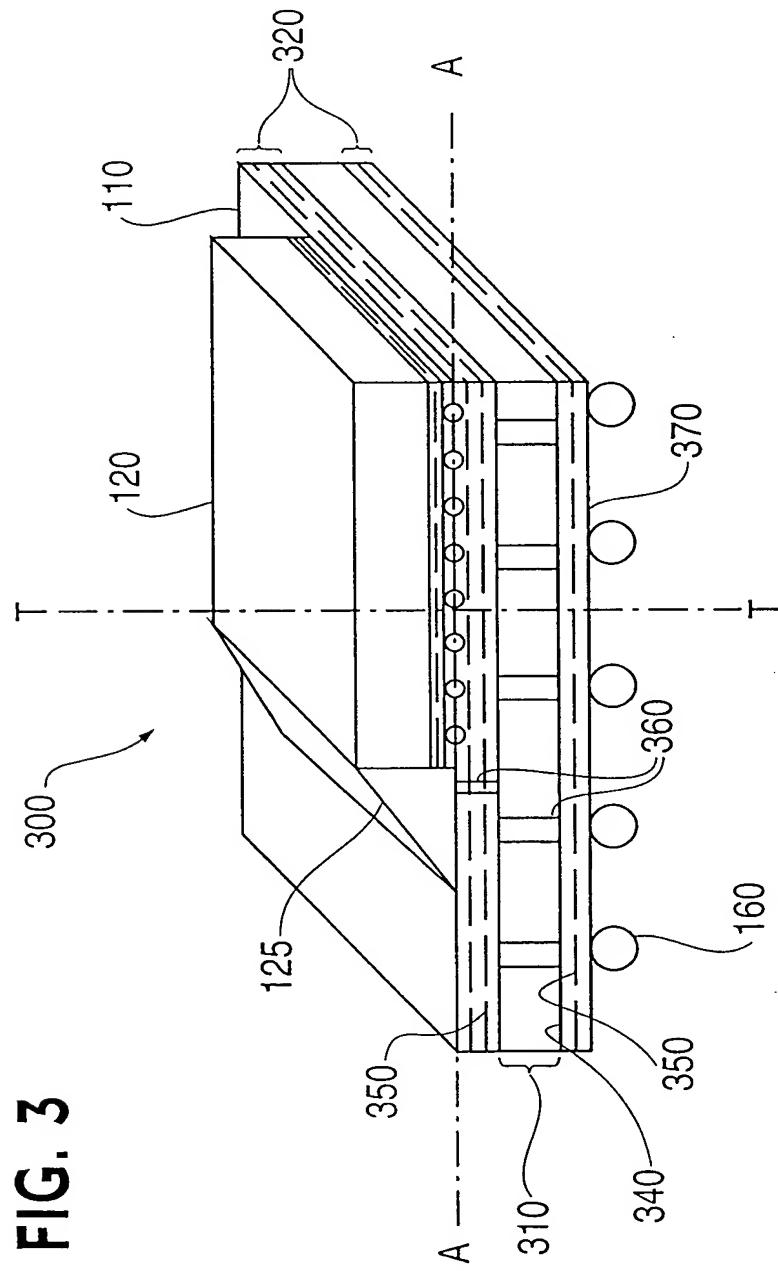


FIG. 2





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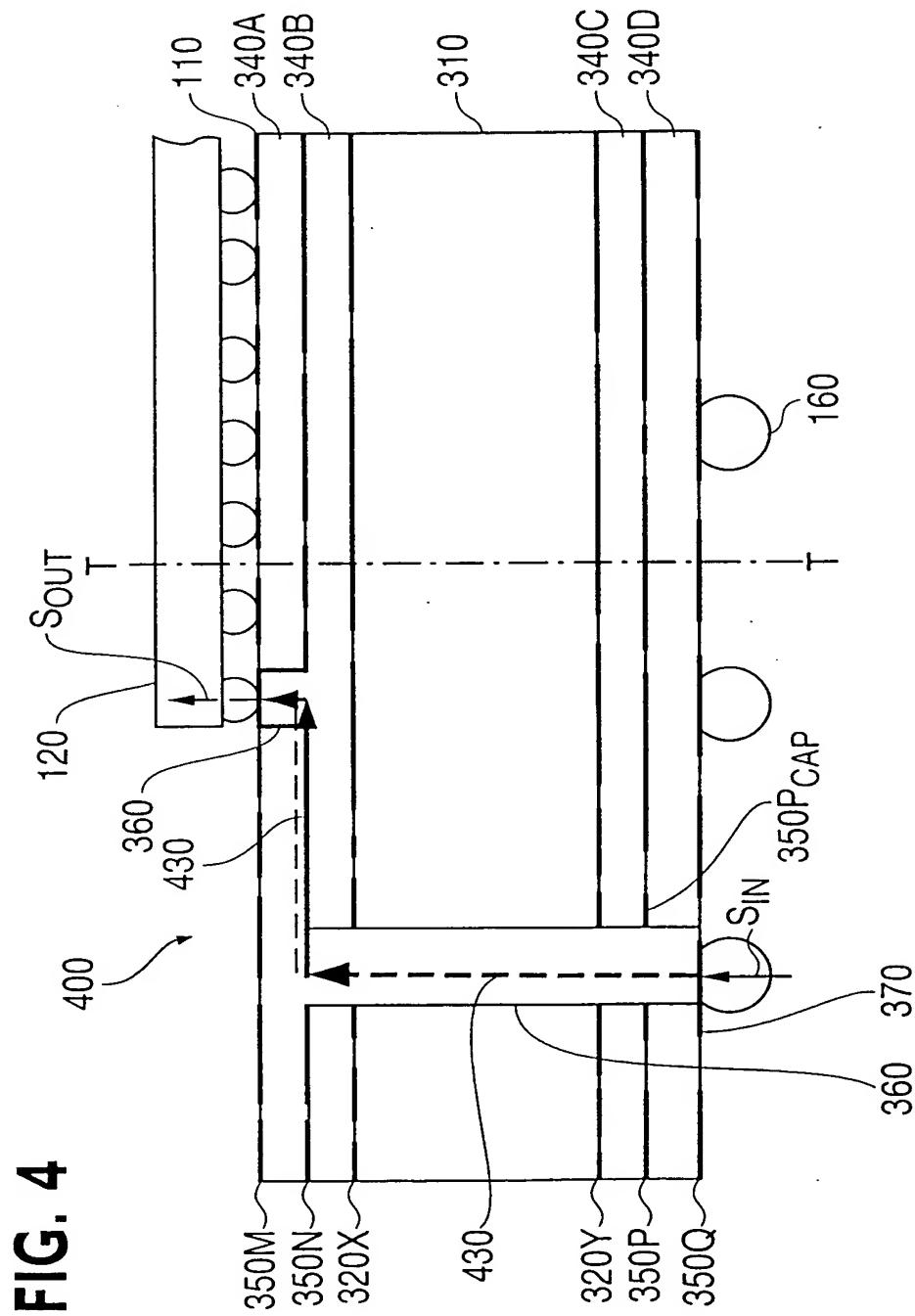


FIG. 5

